

Docket No.: MAS-FIN-419



LPH

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on the date indicated below:

By: Werner Stemmer

Date: August 26, 2004

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applic. No. : 10/723,906 Confirmation No: 1149
Applicant : Wolfgang Hetzel et al.
Filed : November 26, 2003
Title : Electronic Component Having Stacked Semiconductor Chips in Parallel, and a Method for Producing the Component
Art Unit : 2811
Examiner : Hung K. Vu

Docket No. : MAS-FIN-419
Customer No. : 24131

R E S P O N S E

Hon. Commissioner for Patents

Sir:

Responsive to the Office Action dated August 5, 2004, the following remarks are made:

In deference to the restriction requirement on pages 2 and 3 of the above-identified Office Action, applicants elect group I, claims 1-9, for prosecution at this time.

Applicants request a rejoinder under MPEP §821.04.

In view of the foregoing, the early issuance of an Action on the merits of claims 1-9 is solicited.

Respectfully submitted,

A handwritten signature of Werner H. Stemmer.

For Applicants

WERNER H. STEMER
REG. NO. 34,956

Date: August 26, 2004

Lerner and Greenberg, P.A.
Post Office Box 2480
Hollywood, FL 33022-2480
Tel: (954) 925-1100
Fax: (954) 925-1101
/bmb